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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the U.S. Patent Application of

SAITO et al

Application Number: 10/765,198

FEB 2 4 2006

Filed: January 28, 2004

For: An Accumulation Mode CMOS
Semiconductor Device Having Low
Mobility Reduction (As Amended)

Attorney Docket No. HITA.0501

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

## **COVER LETTER**

Sir:

[x] The fee for submission of claims is calculated as shown below:

For	TOTAL WITH NEW CLAIMS ADDED	TOTAL CURRENTLY ON FILE	CLAIMS ALREADY PAID	RATE	CALCULATION
Total Claims	10	19	(Over 20)	x \$50	0
Independent Claims	2	3	(Over 3)	x \$200	0
MULTIPLE DEPENDENT CLAIM(S)				+ \$360	0
REDUCTION FOR FI	LING BY SMALL ENTITY	(note 37 C.F.R. §§ 1.9, 1.2	7, 1.28).	x ½	
	<del> </del>		TOTAL		0.00

In addition, the below-identified communications are submitted in the above-captioned application or proceeding:

(with Claim Amendments) [ ] Terminal Disclaimer	months
[ ] Substitute Specification [ ] Letter to Draftsperson	
[ ] Preliminary Amendment [ ] Assignment	
[ ] Information Disclosure Statement [ ] Other	

[ ]	Please charge my <b>Deposit Account Number</b> in the amount of to cover the fees for A duplicate copy of this paper is enclosed.		
[x]	A check in the amount of \$1,020.00 for the extension fee is enclosed.		
[x]	The Commissioner is hereby authorized to charge any additional fees associated with this communication, including fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to Deposit Account Number 08-1480.		
	Respectfully submitted,		
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